



Message Passing Interface Support for Parallel Computing

Release 2020 R1

ANSYS Products		Supported MPI	Interconnect	Notations	
64-bit Windows	<input checked="" type="checkbox"/> Windows 10	DANSYS* Mechanical	IBM Platform MPI 9.1.4.5 Intel MPI 2018.3.210 (default)	Consult the MPI vendor for supported interconnect hardware.	* Distributed ANSYS (including AUTODYN and Explicit STR)
	<input checked="" type="checkbox"/> Windows 10	ANSYS Fluent	IBM Platform MPI 9.1.4.5 (default) Intel MPI 2018.3.210 MS-MPI v9.0.1 ***	Protocols are supported for GIGE and Infiniband interconnects.	*** :Limited to shared memory runs
	<input checked="" type="checkbox"/> Windows 10	ANSYS CFX	IBM Platform MPI 9.1.4.5 Intel MPI 2018.3.210 (default)	Protocols are supported for GIGE and Infiniband interconnects.	
	<input checked="" type="checkbox"/> Windows 10	ANSYS Forte	Intel MPI 2018.4.274	Consult the MPI vendor for supported interconnect hardware.	
	<input checked="" type="checkbox"/> Windows 10	ANSYS HFSS, Maxwell & Q3D Extractor	IBM Platform MPI 9.1.4.2 (default) Intel MPI 2018.3.210	Consult the MPI vendor for supported interconnect hardware.	
	<input checked="" type="checkbox"/> Windows 10	ANSYS SPEOS & VRXPERIENCE	IBM Platform MPI 9.1.4.3 MS-MPI v10	Consult the MPI vendor for supported interconnect hardware.	

	ANSYS Products	Supported MPI	Interconnect	Notations	
Windows Server 2012, 2016 & 2019	<input checked="" type="checkbox"/> Windows Server 2016**	DANSYS* Mechanical	Microsoft HPC Pack (MS MPI)	Consult the MPI vendor for supported interconnect hardware.	* Distributed ANSYS (including AUTODYN and Explicit STR) ** Requires HPC pack 2016 update 2
	<input checked="" type="checkbox"/> Windows Server 2016**	ANSYS Fluent	Microsoft HPC Pack (MS MPI)	Protocols are supported for GIGE and Infiniband interconnects.	** Requires HPC pack 2016 update 2
	<input checked="" type="checkbox"/> Windows Server 2016**	ANSYS CFX	Microsoft HPC Pack (MS MPI)	Protocols are supported for GIGE and Infiniband interconnects.	** Requires HPC pack 2016 update 2
	<input checked="" type="checkbox"/> Windows Server 2016 <input checked="" type="checkbox"/> Windows Server 2019	ANSYS Forte	Intel MPI 2018 4.274	Consult the MPI vendor for supported interconnect hardware.	
	<input checked="" type="checkbox"/> Windows Server 2016** <input checked="" type="checkbox"/> Windows Server 2019	ANSYS HFSS, Maxwell & Q3D Extractor	IBM Platform MPI 9.1.4.2 (default) Intel MPI 2018.3.210	Consult the MPI vendor for supported interconnect hardware.	** Requires HPC pack 2016 update 2
	<input checked="" type="checkbox"/> Windows Server 2016** <input checked="" type="checkbox"/> Windows Server 2019	ANSYS SPEOS & VRXPERIENCE	Microsoft HPC Pack (MS MPI) IBM Platform MPI 9.1.4.3 Intel MPI 2019.3.203 MS-MPI v10	Consult the MPI vendor for supported interconnect hardware.	** Requires HPC pack 2016 update 2

ANSYS Products		Supported MPI	Interconnect	Notations	
64-bit Linux	<input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 7.4 / 7.5 / 7.6 / 7.7	DANSYS* Mechanical	IBM Platform MPI 9.1.4.3 Intel MPI 2018.3.222 (default)	Consult the MPI vendor for supported interconnect hardware.	<p>* Distributed ANSYS (including AUTODYN and Explicit STR)</p> <p>** For Omni-Path architecture, Omni-Path software 10.2 or higher is recommended, and Intel MPI is preferred.</p> <p>*** Cray MPI is supported on all Cray XE and XC systems; Linux versions require a minimum of Cray Linux Environment 6.0 update 03 (based on SUSE Linux Enterprise Server 12); MPT versions require Cray MPT 7.x default for Fluent.</p> <p>** For Omni-Path architecture, Omni-Path software 10.2 or higher is recommended, and Intel MPI is preferred.</p> <p>*** Cray MPI is supported on all Cray XE and Cray XC systems: Linux versions require a minimum of Cray Linux Environment 6.0 update 03 (based on SUSE Linux Enterprise Server 12); MPT versions require Cray MPT 7.x default for CFX.</p>
	<input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 7.4 / 7.5 / 7.6 / 7.7	ANSYS Fluent	IBM Platform MPI 9.1.4.3 (default) Intel MPI 2018.3.222** OpenMPI 3.1.2 Cray MPI***	Protocols are supported for GIGE and Infiniband interconnects, including Omni-Path fabric.	
	<input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 7.4 / 7.5 / 7.6 / 7.7	ANSYS CFX	IBM Platform MPI 9.1.4.3 Intel MPI 2018.3.222** (default) Cray MPI***	Protocols are supported for GIGE and Infiniband interconnects, including Omni-Path fabric.	
	<input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 7.4 / 7.5 / 7.6 / 7.7	ANSYS Forte	Intel MPI 2018.4.274	Consult the MPI vendor for supported interconnect hardware.	
	<input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 7.4 / 7.5 / 7.6 / 7.7	ANSYS HFSS, Maxwell & Q3D Extractor	IBM Platform MPI 9.1.4.3 (default) Intel MPI 2018.3.222	Consult the MPI vendor for supported interconnect hardware.	
	<input checked="" type="checkbox"/> Red Hat 6.9 / 6.10 <input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 11 (SP3, SP4) <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 6.9 / 7.4 / 7.5 / 7.6 / 7.7	ANSYS RedHawk	OpenMPI 1.10.7	Consult the MPI vendor for supported interconnect hardware.	
	<input checked="" type="checkbox"/> Red Hat 7.4 / 7.5 / 7.6 / 7.7 <input checked="" type="checkbox"/> SLES / SLED 12 (SP2, SP3, SP4) <input checked="" type="checkbox"/> CentOS 7.4 / 7.5 / 7.6 / 7.7	ANSYS SPEOS & VRXPERIENCE	IBM Platform MPI 9.1.4.3 Intel MPI 2018 4.057	Consult the MPI vendor for supported interconnect hardware.	